

L Number	Hits	Search Text	DB	Time stamp
1	14	((power adj supply adj bus) and (isolat\$3 with (power adj supply))) and ((cpu processor microprocessor) with (power adj supply))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/26 16:38
-	3059	(processor CPU microprocessor) and ((power adj2 supply) with bus) and switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 00:05
-	682	((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 00:01
-	14	((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 00:09
-	0	(processor CPU microprocessor) and ((power adj2 supply) with bus) same (power adj2 signal)) and switch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 00:06
-	3	((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and (MOS or mosfet)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 00:10
-	12	((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and (MOS or mosfet or fet or transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 00:13
-	7	((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and (MOS or mosfet or fet or transistor) with switch)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 00:18
-	3	((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and (MOS or mosfet or fet or transistor) with switch)) and pad	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 00:18
-	532614	(input output) adj2 pad buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 18:06
-	136	((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 18:06
-	122	((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply))) and control	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 18:07
-	71	((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply))) and control) and (transistor FET MOSFET)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/24 13:47

-	2477	((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/24 13:47
-	17	(((((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply))) and control) and (transistor FET MOSFET)) and ((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/24 14:21
-	152	(power adj supply adj bus) and (isolat\$3 with (power adj supply))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/24 14:25
-	11	((power adj supply adj bus) and (isolat\$3 with (power adj supply))) and ((cpu processor microprocessor) with (power adj supply))) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/24 14:26
-	3	(internal with (power adj supply) with bus) and (isolat\$3 adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/26 12:56
-	1	(internal with (power adj supply) with bus) and ((isolat\$3 adj circuit) same (power adj supply))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/26 12:57
-	16	(US-6400595-\$ or US-6377859-\$ or US-6375344-\$ or US-6128682-\$ or US-4551718-\$ or US-6445222-\$ or US-6396137-\$ or US-5338978-\$ or US-4984204-\$ or US-5924081-\$ or US-5224010-\$ or US-5440519-\$ or US-5563838-\$ or US-5383137-\$ or US-4356550-\$ or US-4276594-\$).did.	USPAT	2002/09/26 12:59